UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Atty Dkt. 550-506

C#

EARNSHAW et al

TC/A.U.

2825

Serial No. 10/751,108

Examiner: Whitmore, Stacy

Filed: January 5, 2004 Date: May 18, 2006

Title:

MODELING INTEGRATED CIRCUITS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

### RESPONSE/AMENDMENT/LETTER

This is a response/amendment/letter in the above-identified application and includes an attachment which is hereby incorporated by reference and the signature below serves as the signature to the attachment in the absence of any other signature thereon.

## ☐ Correspondence Address Indication Form Attached.

### Fees are attached as calculated below:

Total effective claims after amendment minus highest number

previously paid for (at least 20) = x \$50.00 \$0.00 (1202)/\$0.00 (2202) \$

Independent claims after amendment

minus highest number x \$200.00 \$0.00 (1201)/\$0.00 (2201) \$ previously paid for (at least 3) =3

If proper multiple dependent claims now added for first time, (ignore improper); add

\$360.00 (1203)/\$180.00 (2203) \$

Petition is hereby made to extend the current due date so as to cover the filing date of this

One Month Extension \$120.00 (1251)/\$60.00 (2251) paper and attachment(s)

Two Month Extensions \$450.00 (1252)/\$225.00 (2252) Three Month Extensions \$1020.00 (1253/\$510.00 (2253)

Four Month Extensions \$1590.00 (1254/\$795.00 (2254)

Five Month Extensions \$2160.00 (1255/\$1080.00 (2255) \$ 120.00

Terminal disclaimer enclosed, add

\$130.00 (1814)/ \$65.00 (2814)

Applicant claims "small entity" status. ☐ Statement filed herewith

Rule 56 Information Disclosure Statement Filing Fee \$180.00 (1806)

Assignment Recording Fee \$40.00 (8021)

Other: \$

> TOTAL FEE ENCLOSED \$ 120.00

The Commissioner is hereby authorized to charge any deficiency, or credit any overpayment, in the fee(s) filed, or asserted to be filed, or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Account No. 14-1140. A duplicate copy of this sheet is attached.

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NIXON & VANDERHYE P.C.

By Atty: John R. Lastova, Reg. No. 33,149

Signature:

## THE UNITED STATES PATENT AND TRADEMARK OFFICE

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EARNSHAW et al

Atty. Ref.: 550-506; Confirmation No. 1346

Appl. No. 10/751,108

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For: MODELING INTEGRATED CIRCUITS

May 18, 2006

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

# <u>AMENDMENT</u>

Responsive to the Official Action dated January 18, 2006, (for which a one month extension of time is requested), please amend the above-identified application as follows:

## **AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

- 1. (Original) A method of modeling an integrated circuit, said method comprising the steps of:
- (i) generating a circuit component model including signal transitions with a set of associated delays and rules for a given implementation of said integrated circuit;

05/19/2006 HALI11 00000062 10751108

01 FC:1251

120.00 OP